

<64B	64-127 B	128-255 B	256-511 B	512-1K-1	1K-2K-1	2K-4K-1	4K-8K-1
<64	<128	<256	<512	<1024	<2048	<4096	<8192
1	<96	<224	<480	<992			
	<192	<448	<960				
2	<160	<416	<928				
		<384	<896				
		<352	<864				
		<320	<832				
		<288	<800				
			<768				
			<736				
			<704				
			<672				
			<640				
			<608				
			<576				
			<544				
			16				
				<1056	<2080	<5128	
					32	64	128

Fig. 1

A bit Y of the first stage controls 8 bits of the second stage. Y=1 if one of the 8-bit second stage is set to 1

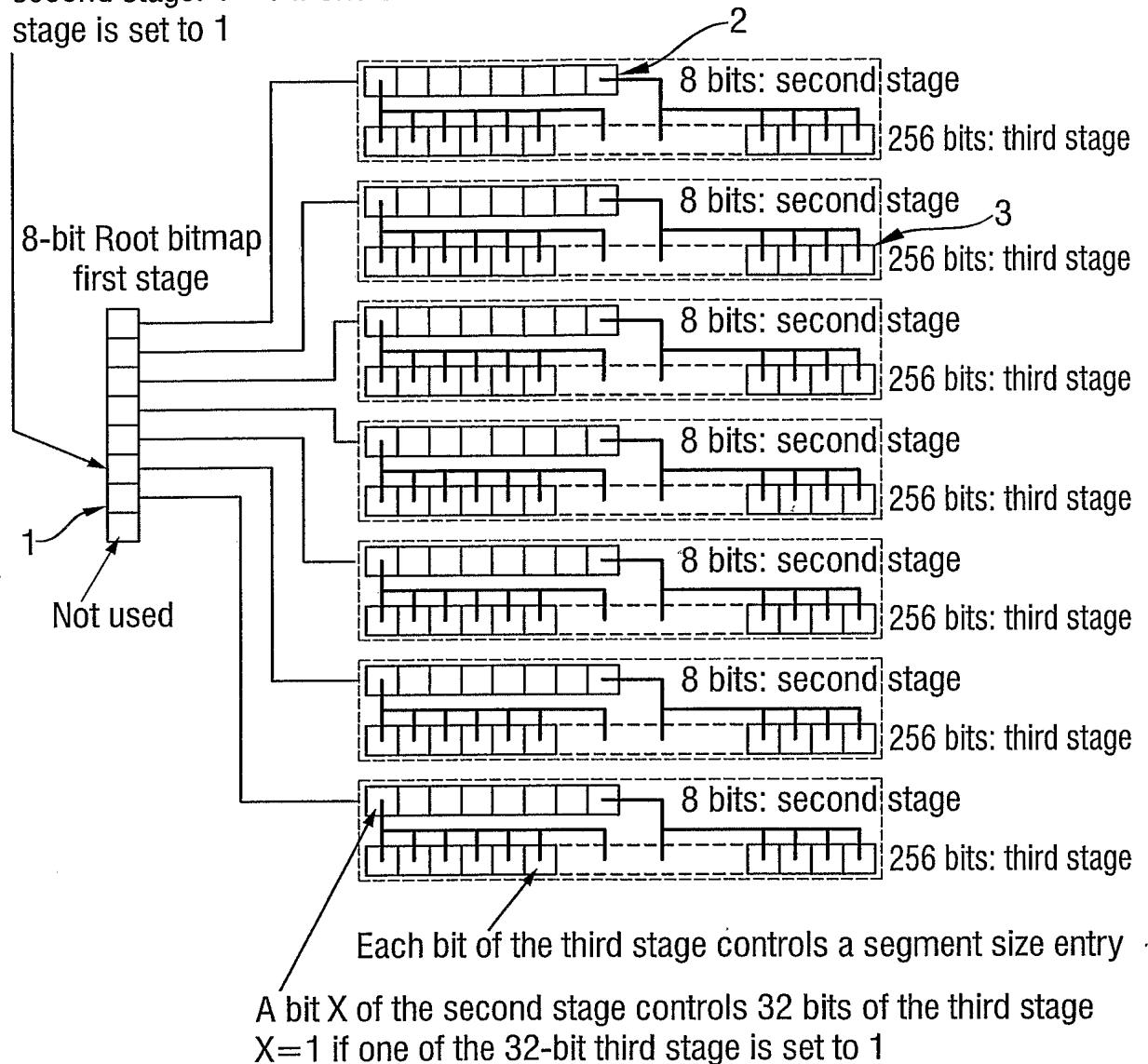
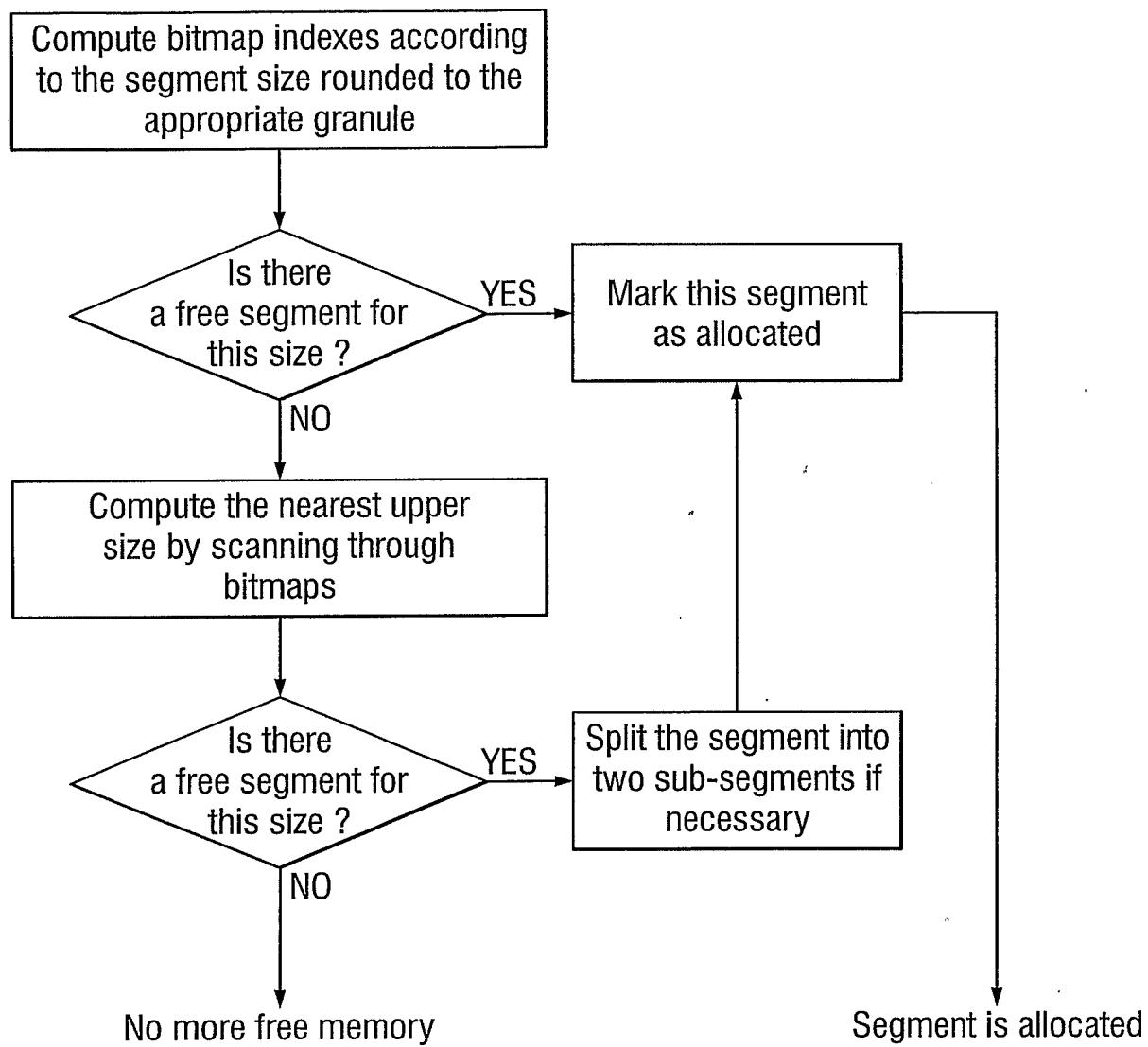
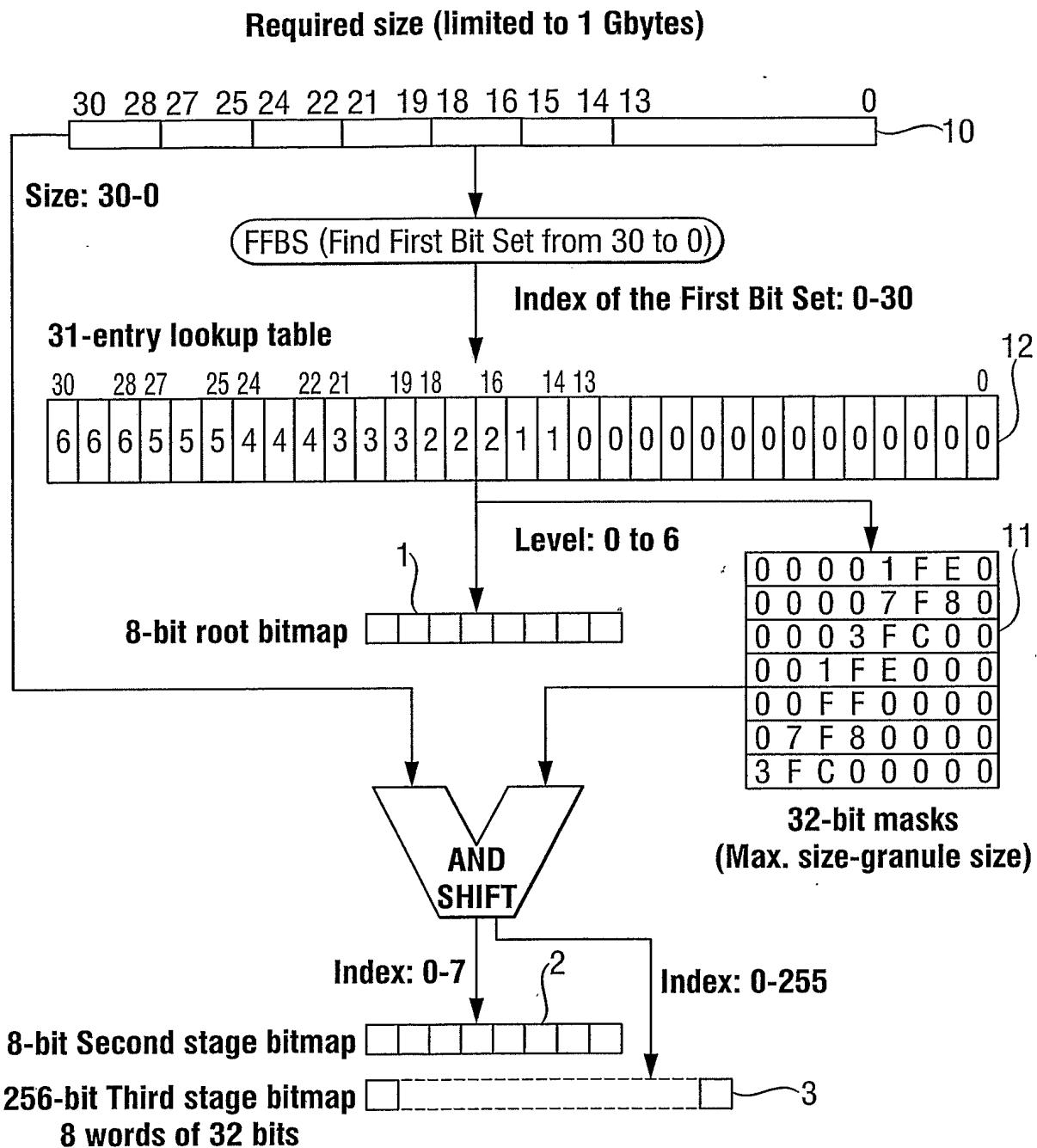


Fig. 2

***Fig. 3***

**Fig. 4**

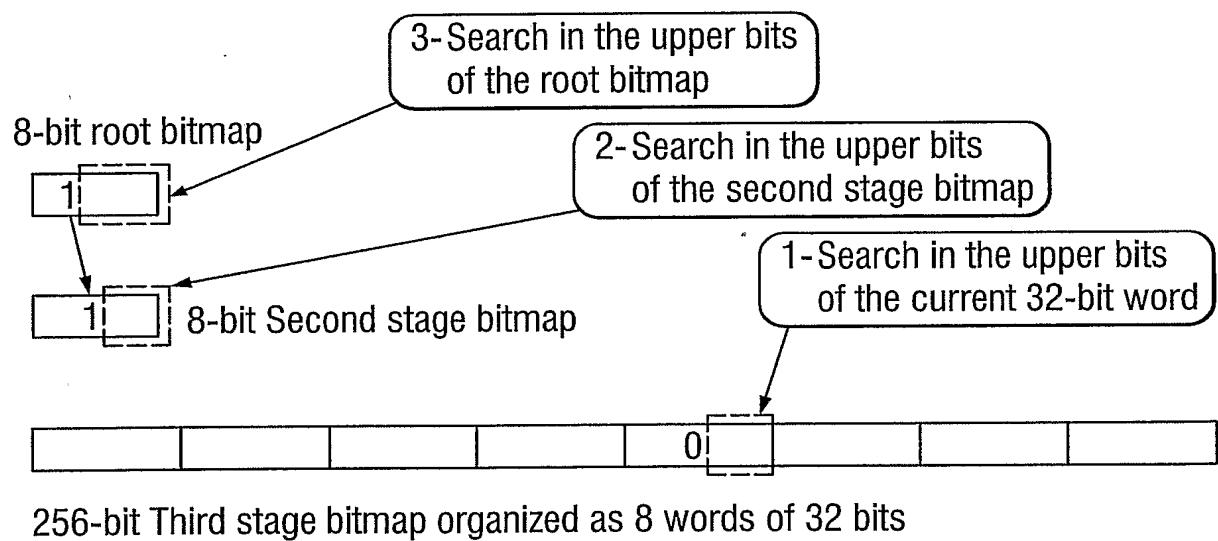
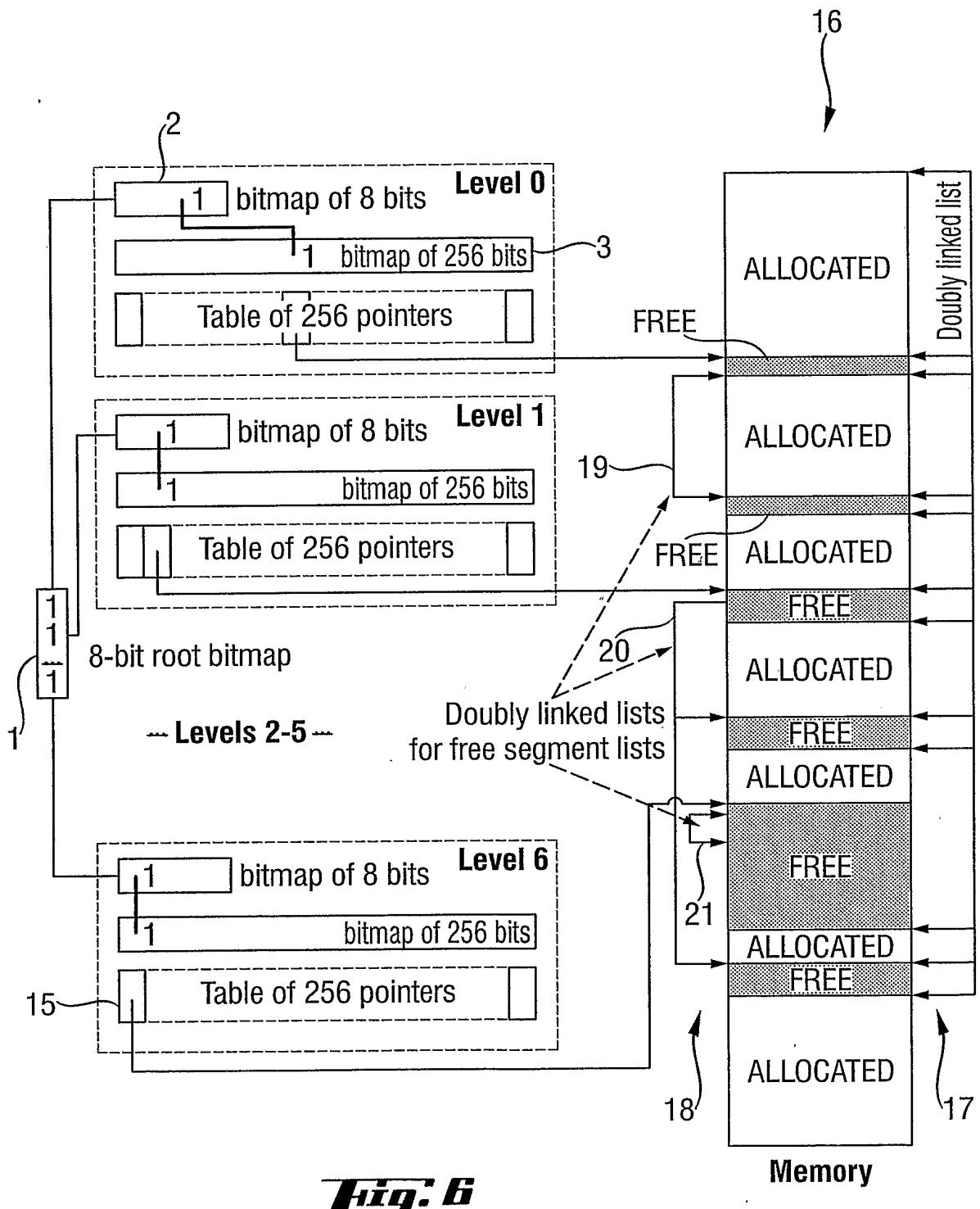
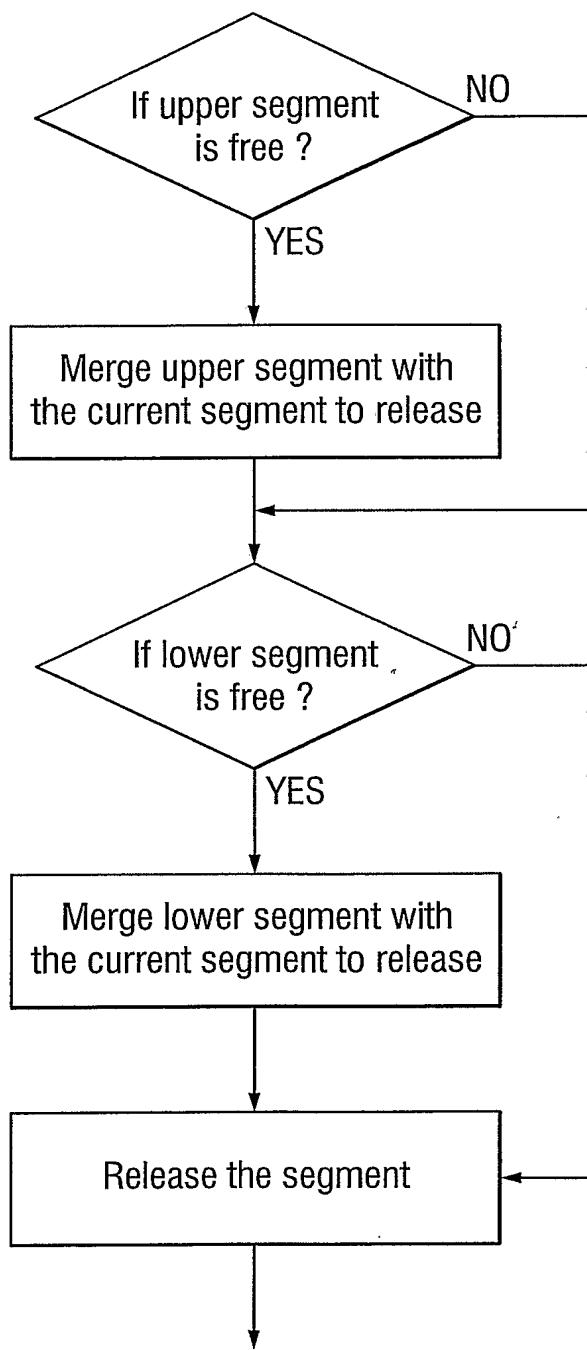
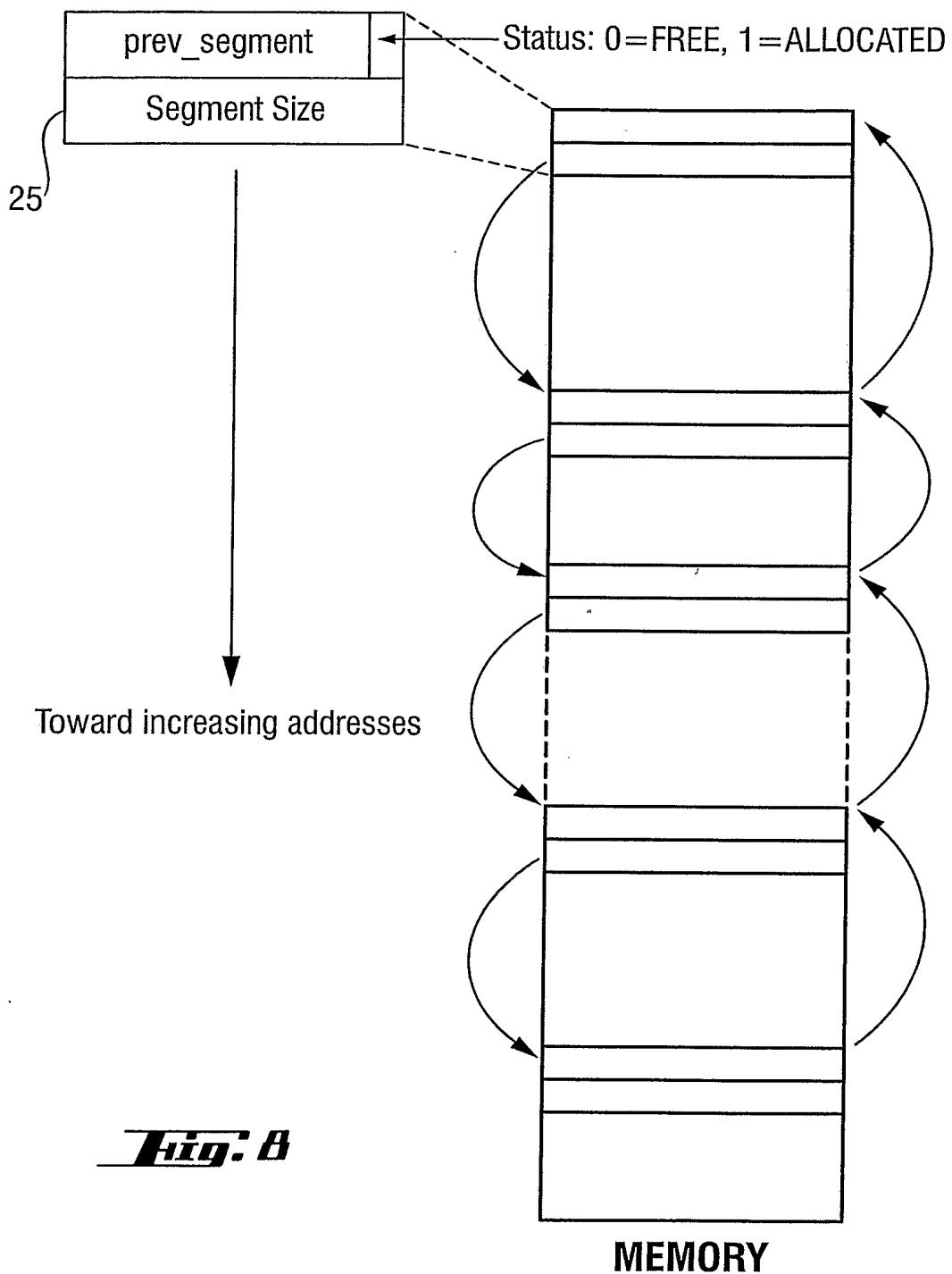


Fig. 5

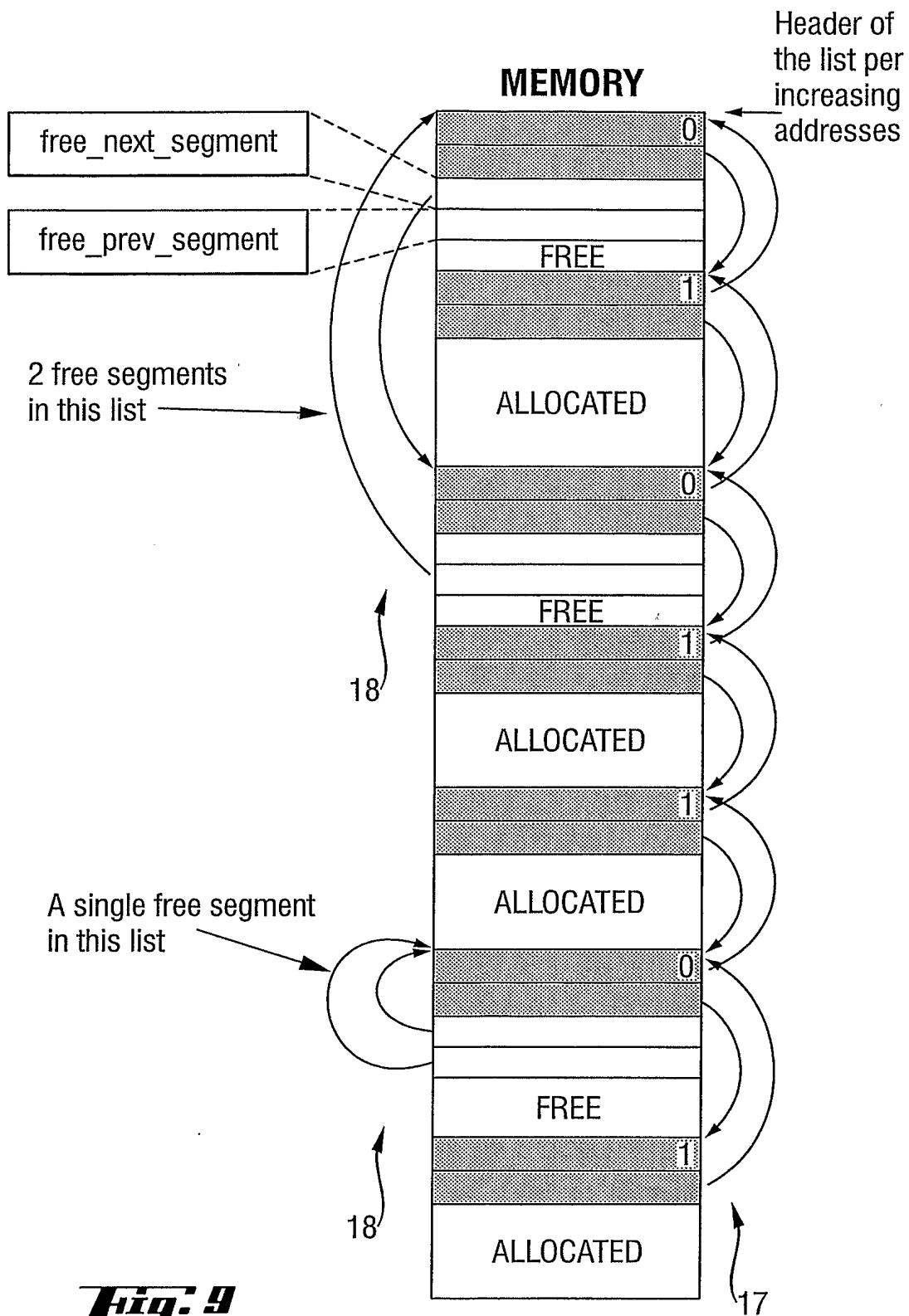
**Fig. 6**

7 / 9

**Fig. 7**



9 / 9

**Fig. 9**